

ABSTRACT

According to one aspect of the invention, a method is provided in which a plurality of data signals are

5 transmitted in parallel mode via a parallel bus from a first device to a second device. Phase information of each data signal received at the second device is detected against a corresponding clock signal. The phase

10 information is sent from the second device to the first device. At the first device, an output delay of each data signal is adjusted based on the phase information received from the second device.